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Ettus Research™ USRP™ B100 Software Defined Radio

The Ettus Research USRP B100 builds on the success of the USRP1 featuring a larger, more powerful FPGA, flexible sample rates, and onboard TCXO clock reference. This system connects to a host computer by USB 2.0 therefore providing up to 16 MHz of RF bandwidth.



Ettus Research USRP B100

It contains a programmable FPGA, two 64 MS/s 12-bit ADCs, two 128 MS/s 14-bit DACs, and auxiliary analog and digital I/O to facilitate integration into a larger system. The USRP B100 can be synchronized to an external clock reference and pulse-per-second (PPS) signal.

Features

- 16 MHz of RF bandwidth with 8 bit samples
- 8 MHz of RF bandwidth with 16 bit samples
- USB 2.0 High Speed connectivity
- Motherboard has one RTX daughterboard slot (1 RX + 1 TX connectors)
- Onboard FPGA processing
- FPGA: Xilinx Spartan 3A-1400 FPGA
- ADCs: 12-bits 64 MS/s
- DACs: 14-bits 128 MS/s
- Ability to lock to external 5 or 10 MHz clock reference
- TCXO Frequency Reference (~2.5ppm)
- Flexible clocking from 10 MHz to 64 MHz
- FPGA code can be changed with the free Xilinx® ISE® WebPACK™ tools